Important deadlines:

- Submission (full paper)    July 8, 2025
- Notification              August 24, 2025
- Registration             September 7, 2025
- Camera ready             September 21, 2025

Tracks:

- Trends on hot research/industrial areas
- Field-Programmable Gate Arrays (FPGAs)
- Semiconductors and applications
- Design, models and languages
- Signal processing circuits
- Arithmetic computational circuits
- Microelectronics
- Electronics technologies
- Special circuits
- Consumer electronics
- Application-oriented electronics