

International Academy, Research, and Industry Association

## Best Paper Award

## VHDL Design Tool Flow for Portable FPGA Implementation By

Vijaykumar Guddad, Alexandra Kourfali, Dirk Stroobandt

Presented during CENICS 2019, The Twelfth International Conference on Advances in Circuits, Electronics and Micro-electronics, held in Nice, France during October 27 - 31, 2019.

