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Special track

**CTC-Gov-CRS: Critical Test Capabilities for Informed ICT Governance of Cyber-Resilient Systems**

**Chair and Organizer**
Dr Keith Joiner, Australian Cyber Security Centre (ACSC), University of New South Wales (UNSW), Canberra, Australia
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along with

**CYBER 2018, The Third International Conference on Cyber-Technologies and Cyber-Systems**
November 18 - 22, 2018 - Athens, Greece

Difficulties with ICT projects abound in all parts of the World, with research reporting as many as one in six such projects, often called ‘Black Swans,’ exhibiting cost and schedule overruns in excess of 200 percent. There are also reports of many high-profile ICT projects experiencing high incidences of unexpected cyber vulnerabilities. These project problems and cyber vulnerabilities have not lessened the pace of advanced software functionality in all aspects of governments and society; to such an extent some research is predicting a new Synthetical Age. Collectively these factors have seen renewed interest in ICT governance, from areas as diverse as programme management offices, departmental reform, and high-assurance security. Some of the proposed governance models considered have great complexity and isolation to ICT-only organisational structures in attempts to build prophetic and prescient oversight, while others appeal to simplicity for success.

ICT Governance and cyber resilience, thereof, critically depend on testing to make informed decisions and trade-offs, especially early in life-cycle using software modelling and simulation where necessary. Governance research and evolving practice therefore seek to leverage existing ICT testing to build more cyber-resilient (robust) systems. The biggest challenges, especially for internet-facing architectures, is building and maintaining sufficiently representative test architectures to do, and keep doing regularly, Cyber-Vulnerability and Penetration Assessment (CVPA) testing to the most representative cyber-attack surface possible.

**Topics include, but not limited to:**
- Usability testing
- System integration testing
- Performance (stress) testing
- High-assurance testing
- CVPA testing
- Preview evaluation on any or all of the above using early prototype ICT versions
- Decision-making frameworks for across the software development lifecycle
- Cultural cybersecurity reform evaluation
- Synergising all of the above for more cyber-resilient systems
Important Datelines
Inform the Chair: As soon as you decided to contribute
Submission: October 5
Notification: October 20
Registration: October 20
Camera-ready October 30
Note: These deadlines are somewhat flexible, providing arrangements are made ahead of time with the chair.

Contribution Types
- Regular papers [in the proceedings, digital library]
- Short papers (work in progress) [in the proceedings, digital library]
- Posters: two pages [in the proceedings, digital library]
- Posters: slide only [slide-deck posted on www.iaria.org]
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- See: http://www.iaria.org/format.html
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Publications
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Registration
- Each accepted paper needs at least one full registration, before the camera-ready manuscript can be included in the proceedings.
- Registration fees are available at http://www.iaria.org/registration.html

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