

International Academy, Research, and Industry Association

Best Paper Award

Ultra-Low-Voltage Dual-Rail NAND/NOR for High Speed Processing

Ole Herman Schumacher Elgesem, Omid Mirmotahari, Yngvar Berg

Presented during CENICS 2018, The Eleventh International Conference on Advances in Circuits, Electronics and Micro-electronics, held in Venice, Italy during September 16 - 20, 2018.

